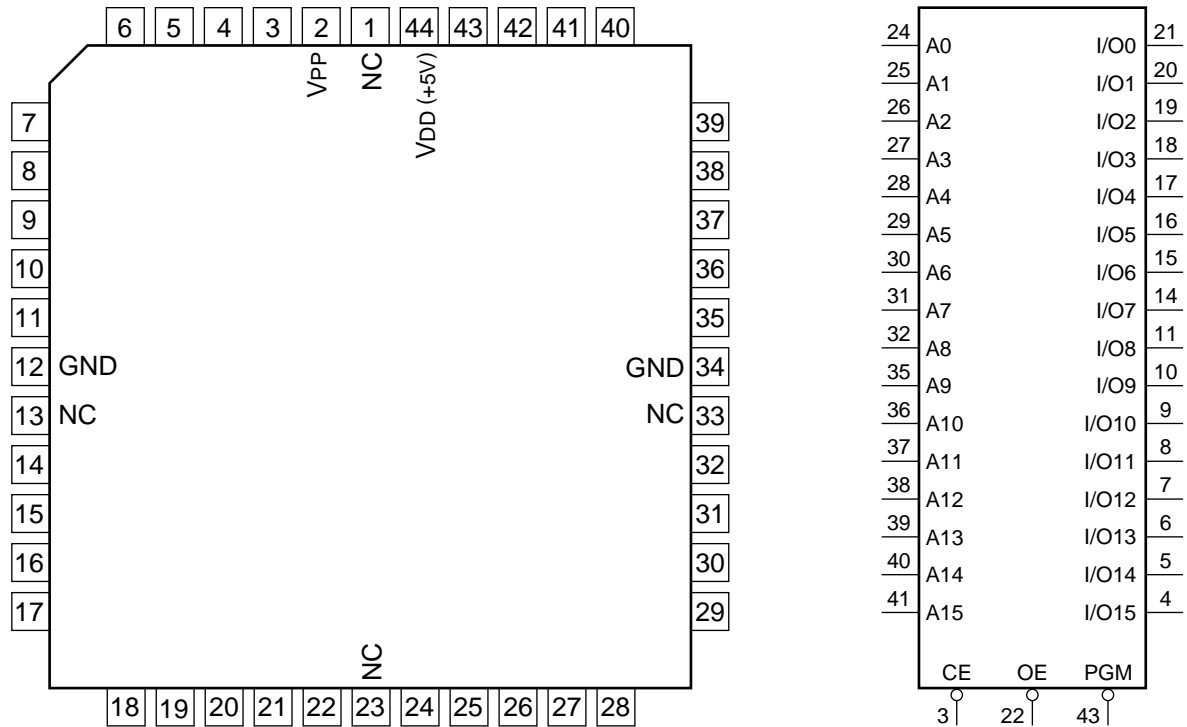

C-MOS 1M (65,536 X 16)-BIT ONE TIME EPROM
-TOP VIEW-



(VDD = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	NC	12	—	GND	23	—	NC	34	—	GND
2	—	VPP	13	—	NC	24	I	A0	35	I	A9
3	I	\overline{CE}	14	I/O	I/O7	25	I	A1	36	I	A10
4	I/O	I/O15	15	I/O	I/O6	26	I	A2	37	I	A11
5	I/O	I/O14	16	I/O	I/O5	27	I	A3	38	I	A12
6	I/O	I/O13	17	I/O	I/O4	28	I	A4	39	I	A13
7	I/O	I/O12	18	I/O	I/O3	29	I	A5	40	I	A14
8	I/O	I/O11	19	I/O	I/O2	30	I	A6	41	I	A15
9	I/O	I/O10	20	I/O	I/O1	31	I	A7	42	—	NC
10	I/O	I/O9	21	I/O	I/O0	32	I	A8	43	I	\overline{PGM}
11	I/O	I/O8	22	I	\overline{OE}	33	—	NC	44	—	VDD

- A0-A15 ; ADDRESS INPUTS
- CE ; CHIP ENABLE INPUT
- I/O0-I/O15 ; DATA INPUTS/OUTPUTS
- \overline{OE} ; OUTPUT ENABLE INPUT
- PGM ; PROGRAM ENABLE INPUT
- VPP ; PROGRAM VOLTAGE INPUT

